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## REMARKS

The present application was filed on September 26, 2003 with claims 1 through 25. Claims 1 through 25 are presently pending in the above-identified patent application. Claims 1, 13, and 25 are proposed to be amended and claims 3 and 15 are proposed to be cancelled herein.

In the Office Action, the Examiner rejected claims 1-3, 13-15, and 25 under 35 U.S.C. §102(e) as being anticipated by Cruz-Albrecht et al. (United States Patent Application Publication Number 2002/0183009), rejected claims 4 and 16 under 35 U.S.C. §103(a) as being unpatentable over Cruz-Albrecht et al. in view of Kashima (United States Patent Number 6,659,356), rejected claims 5, 7-12, 17, and 19-24 under 35 U.S.C. §103(a) as being unpatentable over Cruz-Albrecht et al. in view of Schmidt (United States Patent Application Publication Number 2002/0196029), and rejected claims 6 and 18 under 35 U.S.C. §103(a) as being unpatentable over Cruz-Albrecht et al. in view of Mowery (United States Patent Number 6,492,897).

Independent Claims 1, 13 and 25

Independent claims 1, 13, and 25 were rejected 35 U.S.C. §102(e) as being anticipated by Cruz-Albrecht et al. In particular, the Examiner asserts that Cruz-Albrecht teaches transmitting a wireless signal from said integrated circuit device to said monitoring station using an antenna associated with said integrated circuit device (Abstract; paragraphs [0019-0020], [0062-0065]; FIG. 5A).

Applicant notes that independent claims 1, 13, and 25 have been amended to incorporate the limitation of claim 3. In rejecting claim 3, the Examiner asserts that Cruz-Albrecht teaches wherein said antenna is a pin on said integrated circuit device (paragraphs [0009-0010]; FIG. 2). First, Applicant notes that Cruz-Albrecht labeled FIG. 2 as prior art.

Second, in the text cited by the Examiner, Cruz-Albrecht teaches that

[0009] FIG. 2 illustrates printed circuit boards coupled to a backplane within a computer subsystem. Circuit boards 204, 206, and 208 are coupled to backplane 202. Circuit traces 216, and 218 located on circuit board 204 and backplane 202, respectively, include multiple traces typical of a bus structure and may include

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traces for a JTAG interface. These circuit traces are coupled between circuit boards 204, 206, and 208 and backplane 202 through connectors 220. Circuit traces 218 may additionally be coupled off of backplane 202 to a system controller or a tester, such as a JTAG tester, through tester interface 222.

5 [0010] Integrated circuit devices 210 and 212 on circuit board 204 and integrated circuit device 214 on circuit board 208 are coupled together through circuit traces 216, 218, and 219. One of these integrated circuit devices may be a master device, which controls the other integrated circuit devices. For example, integrated circuit device 214 may be a central processing unit while integrated circuit devices 210 and 212 may include memory devices and input/output devices.

10 Cruz-Albrecht does *not* disclose or suggest *pins* on an integrated circuit device, and does *not* disclose or suggest wherein an *antenna is a pin* on an integrated circuit device. Independent claims 1, 13, and 25, as amended, require wherein said antenna is a pin on said integrated circuit device.

15 Thus, Cruz-Albrecht et al. do not disclose or suggest wherein said antenna is a pin on said integrated circuit device, as required by independent claims 1, 13, and 25, as amended.

#### Additional Cited References

20 Kashima was also cited by the Examiner for its disclosure of a "Hybrid IC Card" and wherein an antenna is printed on said integrated circuit device. Applicant notes that Kashima is directed to a hybrid IC card having functions both of a contact type IC card, and a noncontact type IC card. Kashima, however, does not address the issue of utilizing a pin on an integrated circuit device as an antenna.

25 Thus, Kashima does not disclose or suggest wherein said antenna is a pin on said integrated circuit device, as required by independent claims 1, 13, and 25, as amended.

Schmidt was also cited by the Examiner for its disclosure of wherein said signal is transmitted in accordance with an 802.11 wireless standard. Applicant notes that Schmidt is directed to testing semiconductor devices in wireless communication devices (page 1, paragraph [0002]). Schmidt, however, does not address the issue of utilizing a pin on an integrated circuit device as an antenna.

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Thus, Schmidt does not disclose or suggest wherein said antenna is a pin on said integrated circuit device, as required by independent claims 1, 13, and 25, as amended.

5 Mowery was also cited by the Examiner for its disclosure of wherein said signal is transmitted in accordance with an ultra wide band wireless standard. Applicant notes that Mowery is directed to a system for communicating information to and from untethered subscriber devices and tethered devices by the use of a power transmission line communication system interfacing to a conventional electric power distribution network. Mowery, however, does not address the issue of utilizing a pin on an integrated circuit device as an antenna.

10 Thus, Mowery does not disclose or suggest wherein said antenna is a pin on said integrated circuit device, as required by independent claims 1, 13, and 25, as amended.

Dependent Claims 2-12 and 14-24

15 Dependent claims 2-3 and 14-15 were rejected under 35 U.S.C. §102(b) as being anticipated by Cruz-Albrecht et al., claims 4 and 16 were rejected under 35 U.S.C. §103(a) as being unpatentable over Cruz-Albrecht et al. in view of Kashima, claims 5, 7-12, 17, and 19-24 were rejected under 35 U.S.C. §103(a) as being unpatentable over Cruz-Albrecht et al. in view of Schmidt, and claims 6 and 18 were rejected under 35 U.S.C. §103(a) as being unpatentable over Cruz-Albrecht et al. in view of Mowery.

20 Claims 2-12 and 14-24 are dependent on claims 1 and 13, respectively, and are therefore patentably distinguished over Cruz-Albrecht et al., Kashima, Schmidt, and Mowery, alone or in combination, because of their dependency from amended independent claims 1 and 13 for the reasons set forth above, as well as other elements these claims add in combination to their base claim.

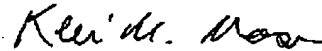
All of the pending claims, i.e., claims 1-25, are in condition for allowance and such favorable action is earnestly solicited.

25 If any outstanding issues remain, or if the Examiner has any further suggestions for expediting allowance of this application, the Examiner is invited to contact the undersigned at the telephone number indicated below.

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The Examiner's attention to this matter is appreciated.

Respectfully submitted,



Date: November 3, 2006

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